

Enclustra announces the Xilinx Zynq-7000 EPP based Mars ZX3 embedded processing module (EPM)

February 28, 2012, Zurich, Switzerland – Embedded solutions provider Enclustra GmbH today announced another step in its continuing effort to help developers easily adopt programmable logic technology, introducing its Mars ZX3 module based on a Xilinx Zynq-7000 Extensible Processing Platform. The new Mars ZX3 embedded processing module (EPM) provides a complete system solution in an industry-standard SO-DIMM form factor (68 x 30 mm).

(Photo: http://www.enclustra.com/download/press/MarsZx3_Blue_300dpi.jpg)

“As a pioneer delivering SO-DIMM sized FPGA modules since 2009, we know firsthand how much development effort our customers can save by using our fool-proof FPGA modules,” said Martin Heimlicher, president and founder of Enclustra. “Engineers need working and stable electronics that enables them to focus on design and development. We see teaming with Xilinx through the Xilinx Alliance Program as a direct way of bringing faster adoption and greater efficiency to Processing-based solutions in the industry.”

“Xilinx Alliance Program Member Enclustra’s adoption of the Zynq-7000 EPP and use of our development environment provides a powerful and flexible embedded solution,” said Mark Jensen Director of Xilinx Processing Platforms Marketing. “Combining their embedded software and FPGA design competencies with the capabilities of the Zynq-7000 EPP solution will enable Enclustra to minimize system footprints and greatly accelerate customer design schedules.”

“We have packed everything we could on the Mars ZX3 module to simplify the life of our customers. This includes the Xilinx Zynq-7000 EPP, up to 1 GB of DDR3 SDRAM, Gigabit Ethernet, USB 2.0 OTG, up to 64 GB of NAND FLASH, oscillators, and all necessary power converters,” said Christoph Glattfelder, Product Manager at Enclustra. “The components used on the Mars ZX3 have been carefully selected for optimal driver support in Linux, Android, FreeRTOS and other widely-used operating systems. The modules will be available in several configurations in commercial as well as in industrial temperature grades.”

About the Mars family of FPGA modules

The Mars ZX3 will be the third member of the Mars family of SO-DIMM sized FPGA modules. The first members included the Mars MX1 and Mars MX2 based on the Spartan-6 family of Xilinx FPGA’s. All Mars modules share a common pinout (with some restrictions) which enables easy migration. The modules are attractively priced for volume quantities and are therefore destined for being integrated into OEM products.

Availability and Pricing

The first engineering sample modules are planned to be available in the first half of 2012 with mass production starting in the fourth quarter of 2012. Single units of the MA-ZX3-10-1C with the Z-7010 EPP, 256 Mbytes of DDR3 SDRAM and 256 Mbytes of NAND FLASH will sell at € 189. Volumes of 10’000 are planned to sell at € 94 in the third quarter of 2013.

About Enclustra

Enclustra FPGA Solutions provides embedded processing modules, IP cores and FPGA development services with a track record in industrial motor drive controls, medical cameras, wireless communications, software defined radio, networking, image processing, application-specific DSP's, and embedded computing. Operating since 2004, the company has a reputation for quality products, comprehensive development services and excellent after-sales support. The company is located in Zurich, Switzerland, a very central location within Europe and works closely with customers worldwide. For more information, visit <http://www.enclustra.com/>.

About Xilinx

Xilinx is the worldwide leader in complete programmable logic solutions. For more information, visit <http://www.xilinx.com/>.

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