

## Press Release

Zurich, 30<sup>th</sup> August 2016

*The Enclustra Mercury+™ KX2: high I/O count and blazing fast memory*

### **Xilinx Kintex-7 module with many I/Os**

*Enclustra presents the Mercury+ KX2: a high I/O count FPGA module based on the Xilinx® Kintex®-7 family. The compact module, which boasts 216 user I/Os and many standard interfaces, is equipped to handle even the most demanding of applications, with a high memory bandwidth of up to 12.5 GByte/sec.*

The Mercury+ KX2 module offers the best performance-to-price-ratio Xilinx Kintex-7 FPGAs in conjunction with high-speed standard interfaces, fast DDR3 SDRAM with a bandwidth of up to 12.5 GByte/sec and many high-speed DSP slices, all in a package measuring just 74 x 54 mm.

The module's 216 user I/Os, 2 GByte DDR3L SDRAM and 64 MByte quad SPI flash combine to form a truly high-performance processing unit. The Mercury+ KX2 is equally well-suited for high-end digital signal processing, communications and networking, and high-speed I/O applications.

A myriad of interfacing options are also available: 8 MGTs with a data transfer rate of up to 10.3125 Gbit/sec, PCIe® Gen2 x8, dual Gigabit Ethernet, and USB 2.0. The module is available in both commercial and industrial temperature ranges, and needs just a single 5-15 V supply for operation.

Enclustra also offers a comprehensive ecosystem for the KX2, offering all required hardware, software and support materials. The Mercury+ PE1 base board is a complete development platform; detailed documentation and reference designs make it easy to get started, in addition to the user manual, schema, a 3D-model, PCB footprint and differential I/O length tables.

Further expansion options are provided by the LPC/HPC FMC connectors on the PE1 base board, compatible with a huge range of add-on boards from

many manufacturers - ADCs, DACs, motor control cards and RF links are just a small selection of possibilities.



*The Enclustra Mercury+ KX2, with Xilinx Kintex-7, is ideally suited to demanding applications which require a high number of I/Os and high memory bandwidth.*

### **About Enclustra GmbH**

Enclustra is an innovative and successful Swiss FPGA design company. With the FPGA Design Center, Enclustra provides services covering the whole range of FPGA-based system development: From high-speed hardware or HDL firmware through to embedded software, from specification and implementation through to prototype production.

In the FPGA Solution Center, Enclustra develops and markets highly-integrated FPGA modules and FPGA-optimized IP cores.

By specialising in forward-looking FPGA technology, and with broad application knowledge, Enclustra can offer ideal solutions at minimal expense in many areas. More information can be found at:

[www.enclustra.com](http://www.enclustra.com)

**Download:** [Press kit \[ZIP\]](#)

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