

## Press Release

Zurich, 14<sup>th</sup> January 2015

*Enclustra Mercury+™ SA2: the Mercury™ SA1's big brother has arrived*

### **Altera Cyclone V SoC module with many I/Os**

*Enclustra will present the Mercury+ SA2 at Embedded World 2016 in Nuremberg; based on the Altera® Cyclone® V SoC, the compact module, which boasts 258 user I/Os, a high-performance processor system and many standard interfaces, is equipped to handle even the most demanding of applications.*

The Enclustra Mercury+ SA2 Altera Cyclone V SoC module combines an ARM® dual-core Cortex™ A9 processor with a fast 28 nm FPGA fabric, all in a package measuring just 74 x 54 mm. The module's 258 user I/Os, 2 GByte DDR3L SDRAM and 64 MByte quad SPI flash combine to form a truly high-performance processing unit. A myriad of interfacing options are also available: 9 MGTs with a data transfer rate of up to 6 Gbps, PCIe® Gen1/Gen2 x4, Gigabit Ethernet, dual Fast Ethernet, USB 3.0 and USB 2.0. The module is available in both commercial and industrial temperature range, and needs just a single 5-15 V supply for operation.

Enclustra also offers a comprehensive ecosystem for the SA2, offering all required hardware, software and support materials. The Mercury+ PE1 base board is a complete development platform for the SA2; detailed documentation and reference designs make it easy to get started, in addition to the user manual, schema, a 3D-model, PCB footprint and differential I/O length tables.

To get Linux up and running in minutes, Enclustra offers the Enclustra Build Environment; Linux can be compiled for the Mercury+ SA2 with a few clicks, generating the required U-Boot, Linux and BusyBox-based root file system binaries.

Further expansion options are provided by the LPC/HPC FMC connectors on the PE1 base board, compatible with a huge range of add-on boards from many manufacturers - ADCs, DACs, motor control cards and RF links are just a small selection of possibilities.



*The Enclustra Mercury+ SA2, with Altera Cyclone V SoC, is ideally suited to demanding applications which require a high number of I/Os.*

### **About Enclustra GmbH**

Enclustra is an innovative and successful Swiss FPGA design company. With the FPGA Design Center, Enclustra provides services covering the whole range of FPGA-based system development: From high-speed hardware or HDL firmware through to embedded software, from specification and implementation through to prototype production. In the FPGA Solution Center, Enclustra develops and markets highly-integrated FPGA modules and FPGA-optimized IP cores. By specialising in forward-looking FPGA technology, and with broad application knowledge, Enclustra can offer ideal solutions at minimal expense in many areas. More information can be found at:

[www.enclustra.com](http://www.enclustra.com)

**Download:** [Press kit \[ZIP\]](#)

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