



## Press Release

20<sup>th</sup> February 2013, Zurich

### **Enclustra announces Linux, eCos, Android and VxWorks on the Mars ZX3 SoC module**

Enclustra is pleased to announce the successful completion of ports of the Linux, eCos, Android and VxWorks operating systems for the Mars ZX3 system on a chip (SoC) module. Ported in collaboration with Enclustra's OS development partners NetModule (Linux, VxWorks) and Ant Micro (eCos, Android), the operating systems showcase the performance and flexibility of the Mars ZX3 module, and will be given their first public exhibition at Embedded World 2013 in Nuremberg on the 26<sup>th</sup> of February.

"From the beginning, the aim of the Mars ZX3 project was to offer a powerful embedded OS platform with a tiny hardware footprint" said Martin Heimlicher, Enclustra's president and founder. "By working closely with Xilinx and our OS development partners, we've delivered on that aim, with a comprehensive solution that allows engineers to quickly take advantage of both the power of the Zynq™-7000 All Programmable SoC and the feature set of the operating system in their own design solutions. For systems employing a TFT display, development time can be reduced further still with our Display Controller IP core, which can also support multi-touch when paired with Linux."

"Certified Xilinx Alliance Program Member Enclustra has greatly widened the appeal of its Zynq-7000 All Programmable SoC based Mars-ZX3 SoC module by porting four further operating systems onto it." said Sanjay Gehani, Senior Manager Alliance Program at Xilinx. "Enclustra's innovative design teams have used the capabilities of the Zynq-7000 SoC solution to create a module that offers a powerful and flexible solution with minimal system footprints and allows customers to bring competitive solutions to market quickly with aggressive design schedules."

#### **Further Information**

##### **About the Mars ZX3 and the Mars family of FPGA modules**

The Mars ZX3 is one of four FPGA modules in the Mars family of all in the industry-standard SO-DIMM format. Based around the Xilinx Zynq-7020 All Programmable SoC, it features 108 user I/Os, Gigabit Ethernet, USB 2.0 OTG, up to 1 GByte of DDR3 SDRAM, and up to 64 GBytes of NAND flash. The Mars family of FPGA modules are attractively priced for volume quantities, and therefore very well-suited for being integrated into products with large production runs.

### **Availability and Pricing**

Engineering sample modules are available now; production modules are expected to ship starting from April 2013. Prices start from €132 per module for volumes of 10000+ units.

### **Other new Enclustra products**

Also on display at Embedded World 2013 will be two of Enclustra's new FPGA modules, the Mars AX3 with Xilinx Artix™-7 and Mercury KX1 with Xilinx Kintex™-7, and FPGA Manager, an integrated software/firmware solution which allows for simple, fast data transfer between host and FPGA over PCIe, USB 3.0 and Gigabit Ethernet interfaces.

### **About Enclustra**

Enclustra FPGA Solutions provides embedded processing modules, FPGA modules, IP cores and FPGA development services.

As well as embedded software, firmware and hardware design, the company has a proven track record in the application areas embedded processing, digital communications, smart vision systems, software defined radio, and motion and drive control.

Since the company was founded in 2004, it has developed a reputation for quality products, comprehensive development services and excellent after-sales support. The company is located centrally in Europe, in Zurich, Switzerland, and continues to expand its close collaboration with customers worldwide. For more information, visit [www.enclustra.com](http://www.enclustra.com).

### **About Xilinx**

Xilinx is the worldwide leader in complete programmable logic solutions. For more information, visit [www.xilinx.com](http://www.xilinx.com).

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